

FIG. 1

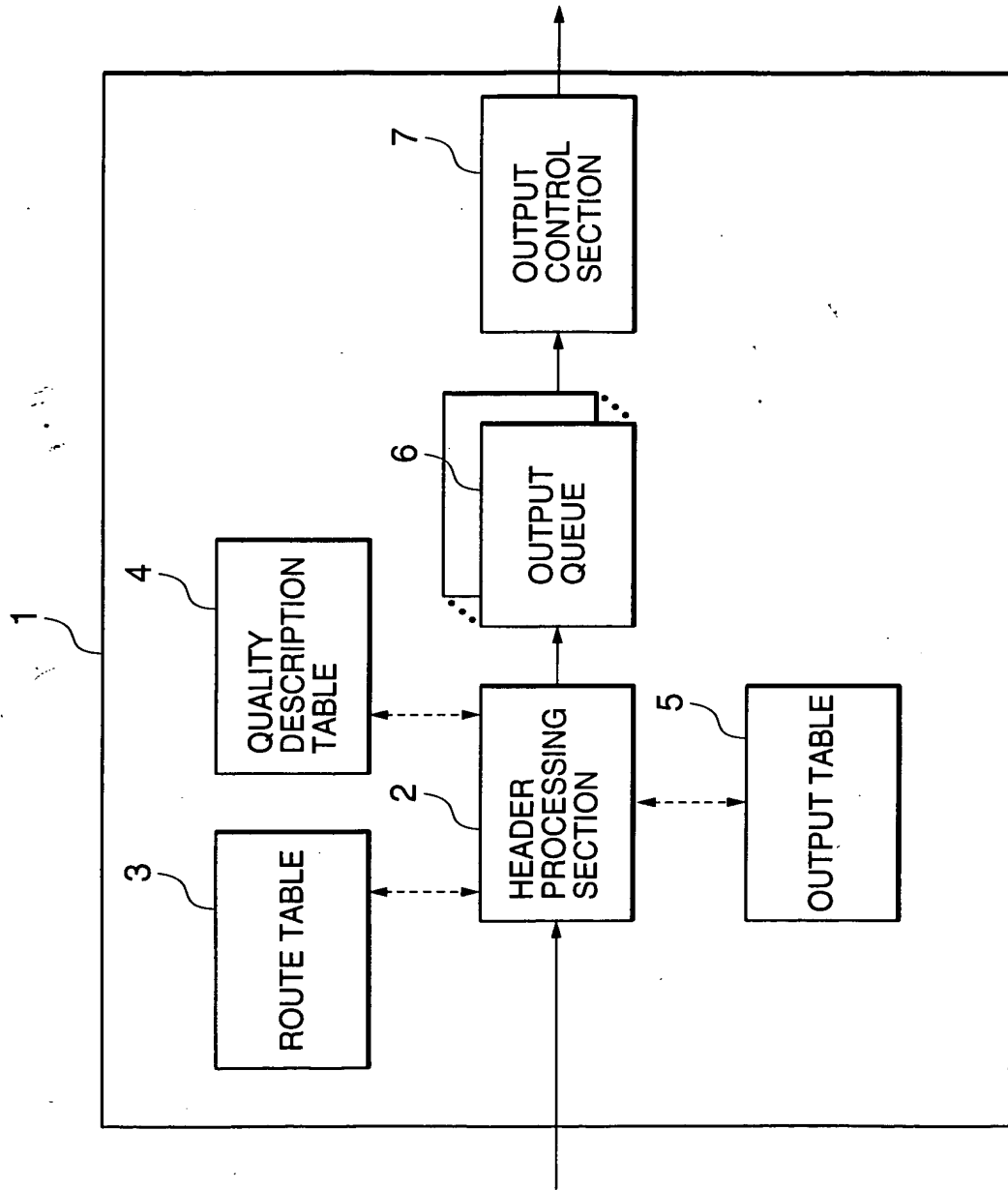


FIG. 2

DESTINATION ADDRESS	MASK LENGTH	OUTPUT DESTINATION

FIG. 3

VIRTUAL DEDICATED NETWORK NUMBER	SOURCE		DESTINATION		QUALITY CLASS
	ADDRESS / MASK LENGTH	FOURTH-LAYER PROTOCOL / PORT NUMBER	ADDRESS / MASK LENGTH	FOURTH-LAYER PROTOCOL / PORT NUMBER	

FIG. 4

OUTPUT DESTINATION	QUALITY CLASS	OUTPUT QUEUE NUMBER	OUTPUT VC NUMBER

FIG. 5

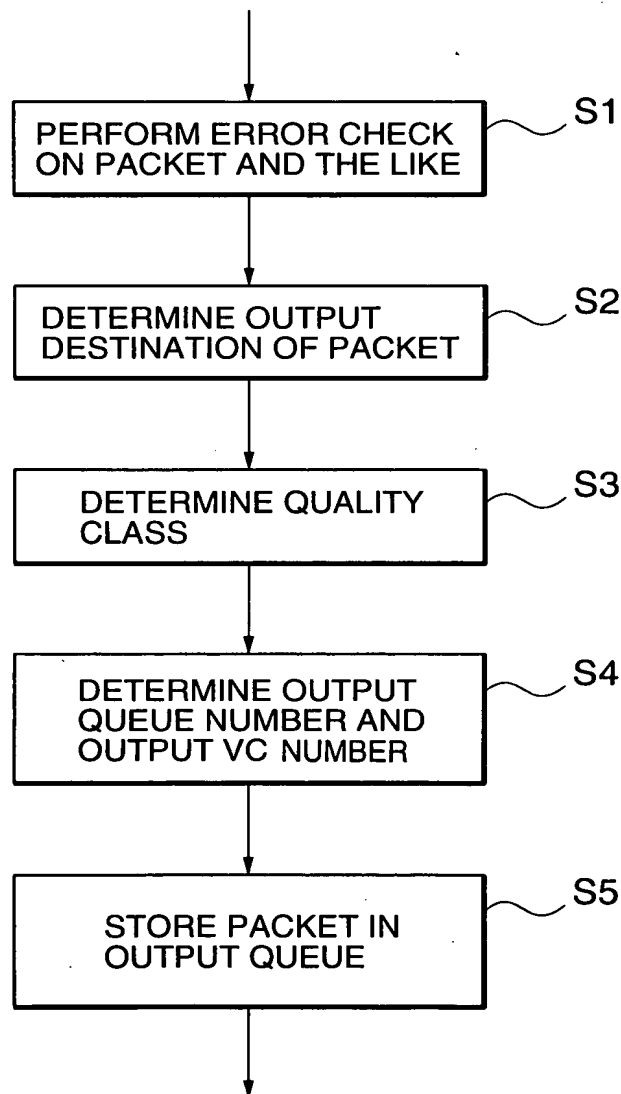


FIG. 6

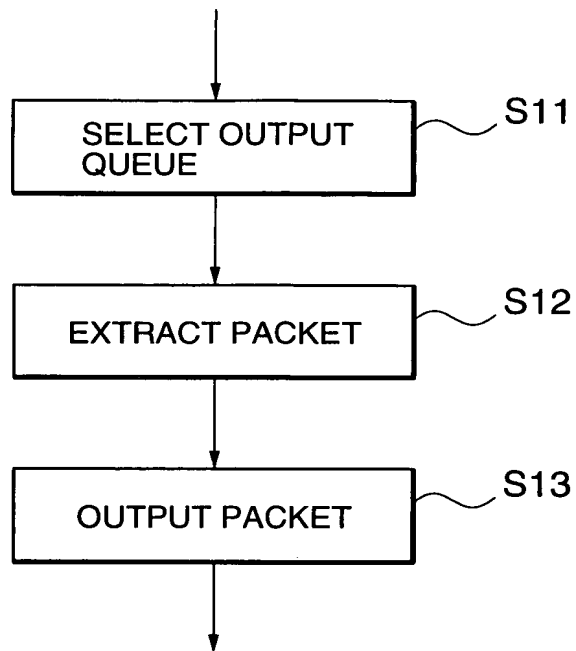
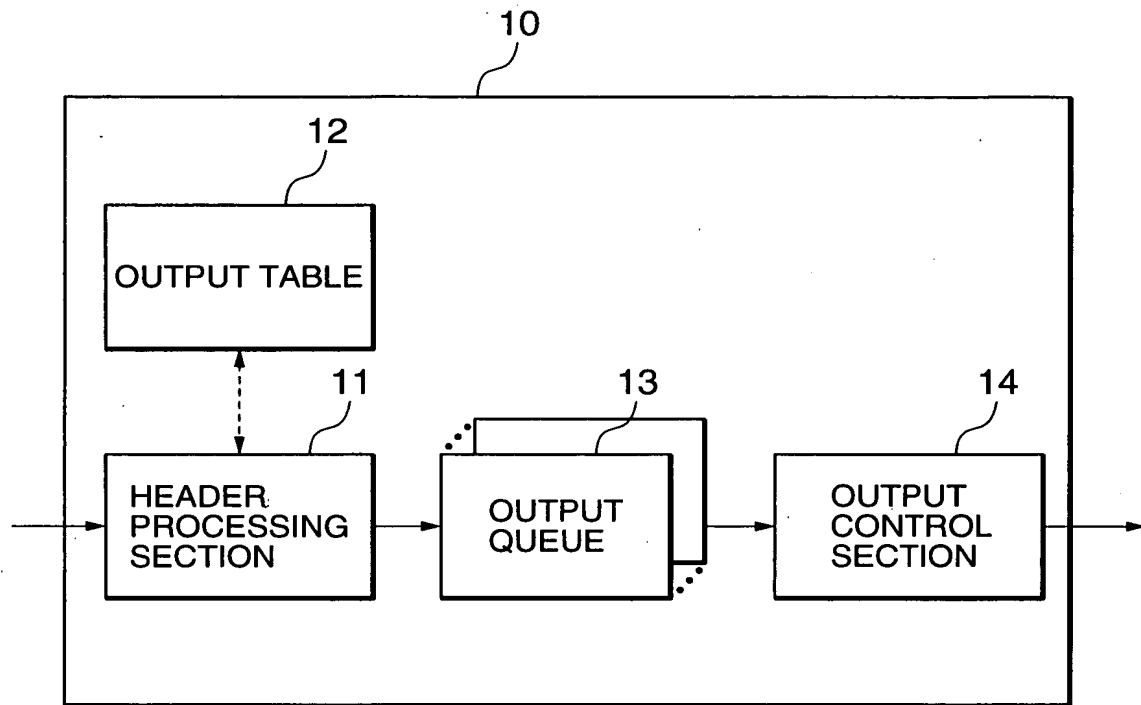


FIG. 7



VIRTUAL DEDICATED NETWORK NUMBER	SOURCE		DESTINATION		OUTPUT QUEUE NUMBER	OUTPUT VC NUMBER
	ADDRESS / MASK LENGTH	FOURTH-LAYER PROTOCOL / PORT NUMBER	ADDRESS / MASK LENGTH	FOURTH-LAYER PROTOCOL / PORT NUMBER		

FIG. 9

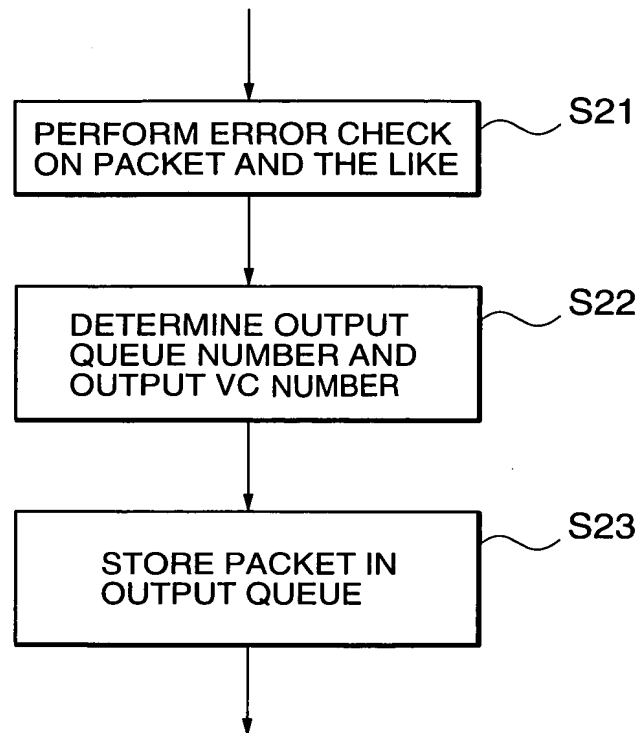


FIG. 10

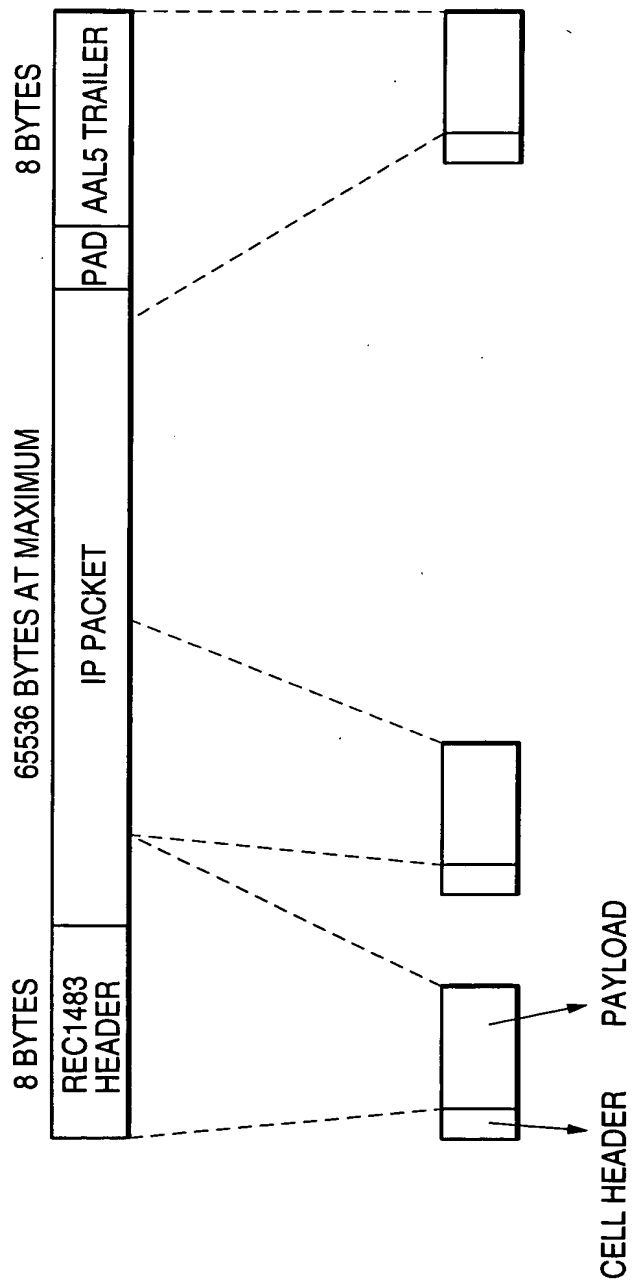
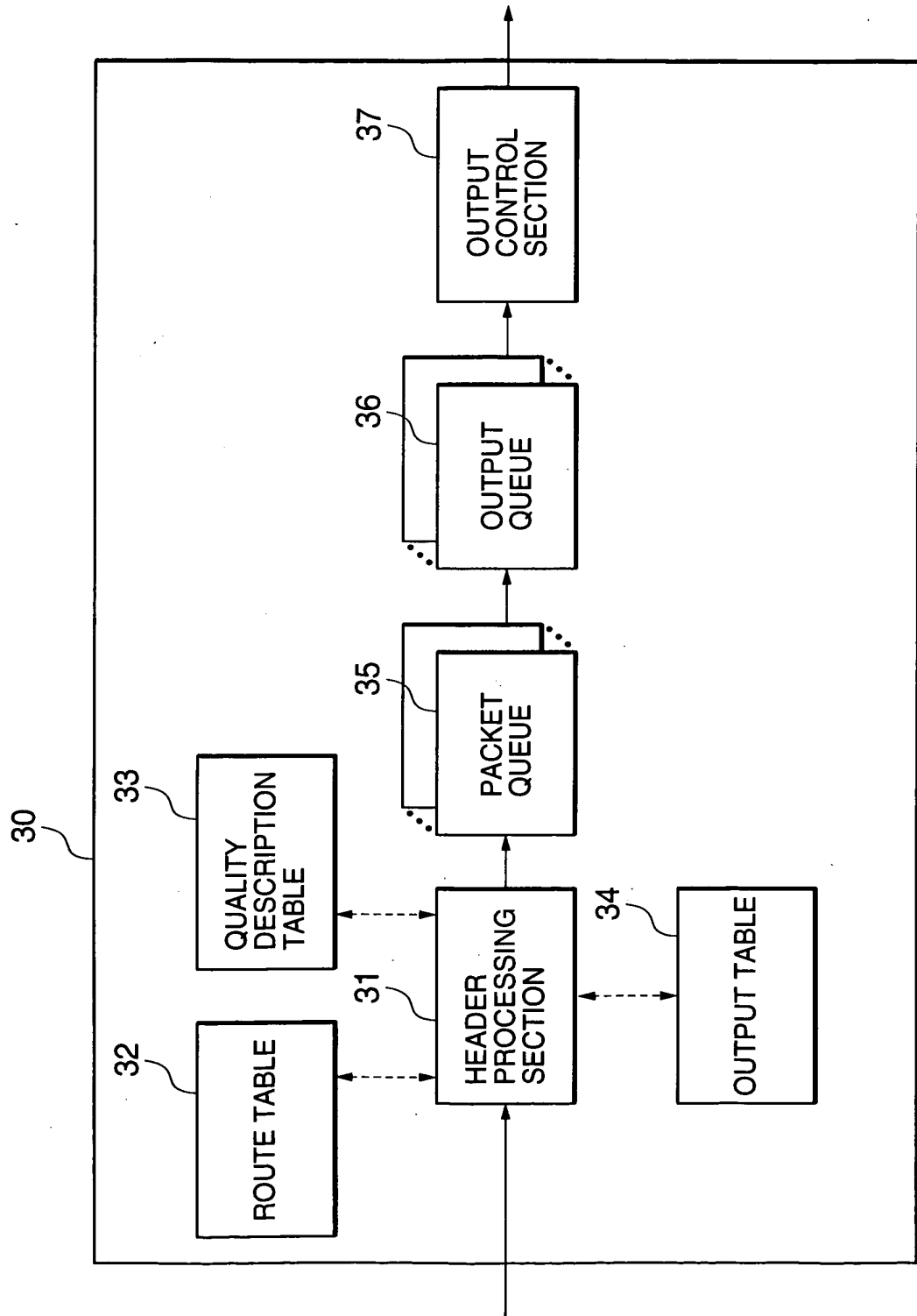


FIG. 11



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FIG. 12

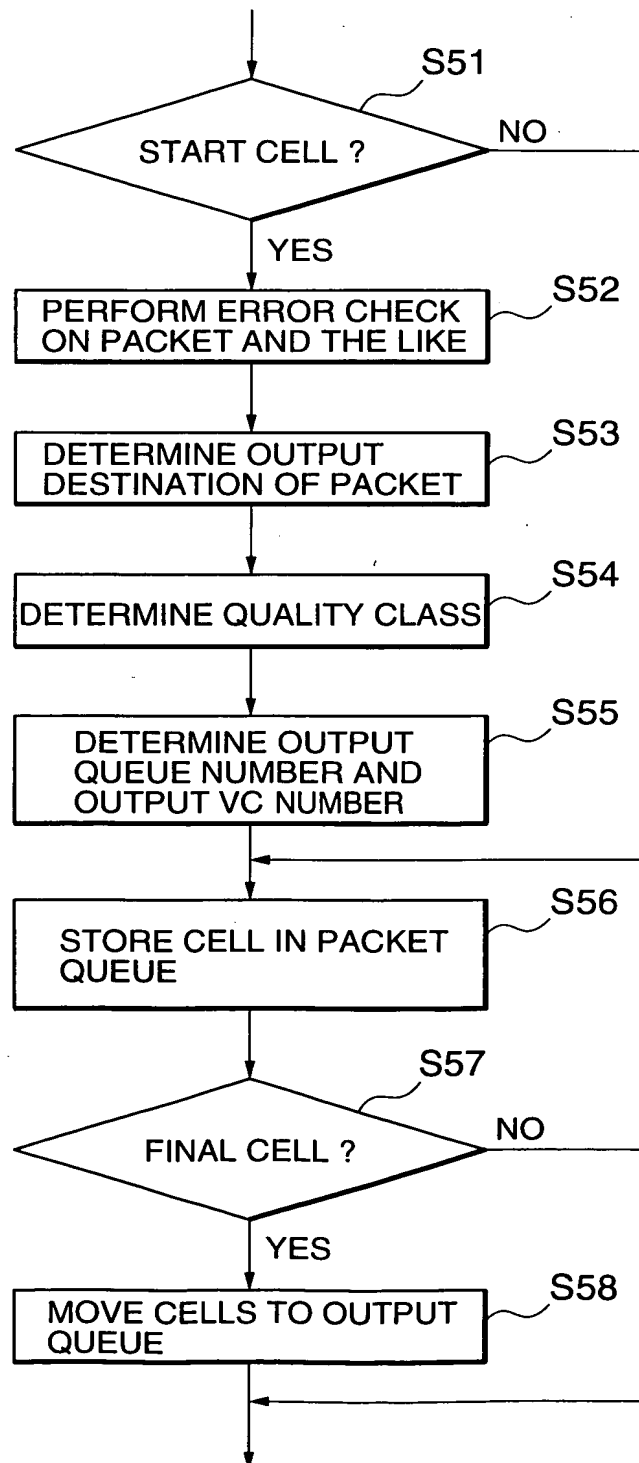
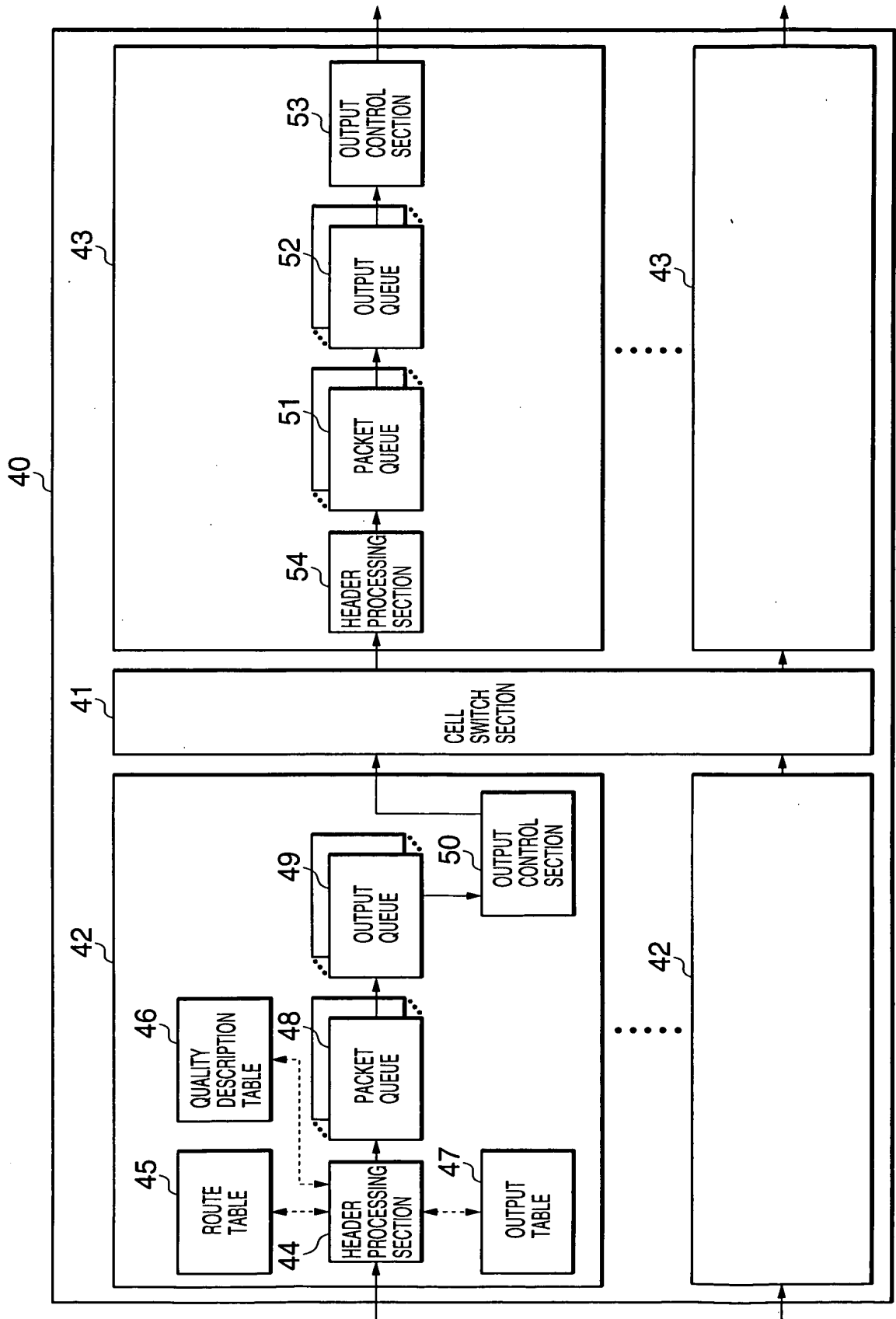


FIG. 13



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FIG. 14

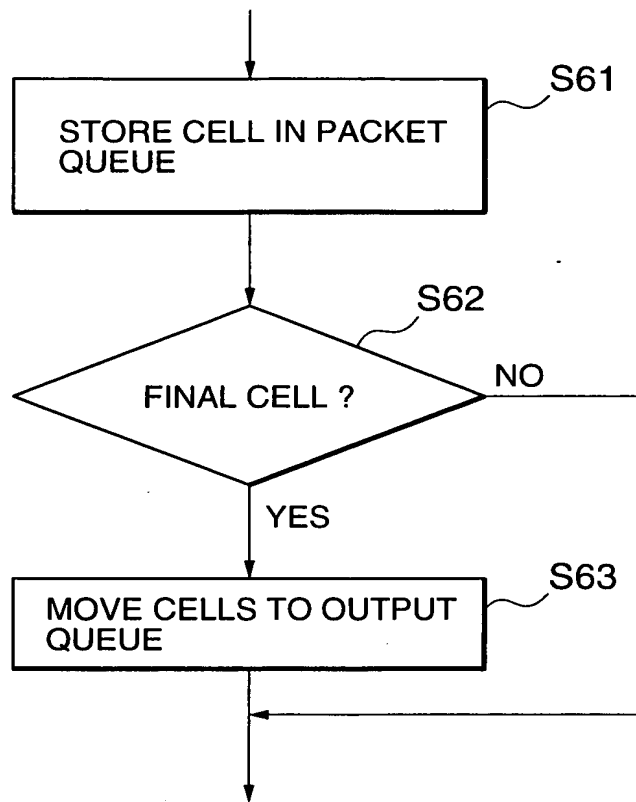


FIG. 15

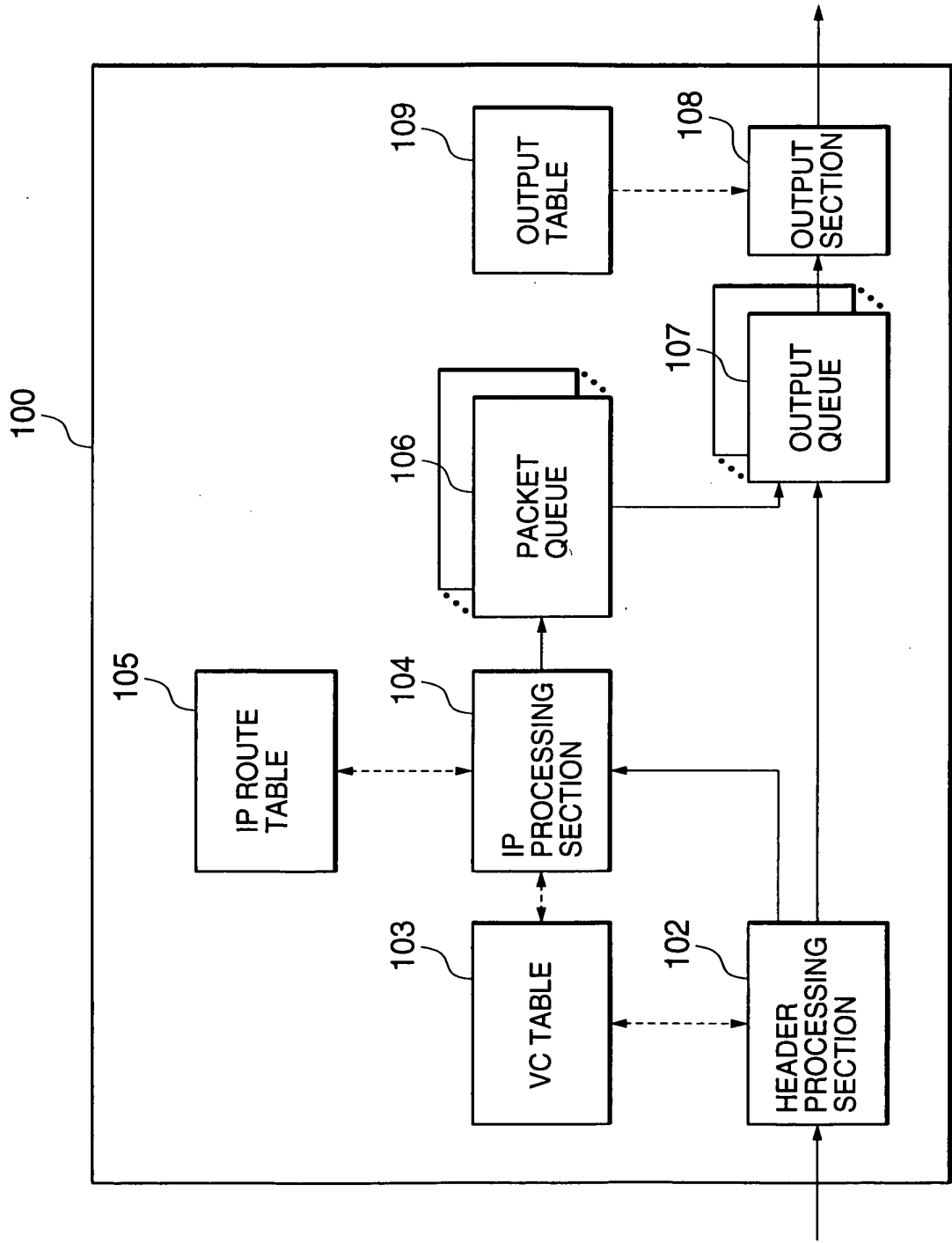


FIG. 16

DESTINATION IP ADDRESS	MASK LENGTH	OUTPUT QUEUE NUMBER

FIG. 17

SOURCE VPI / VCI	IP IDENTIFICATION FLAG	START CELL IDENTIFICATION FLAG	OUTPUT DESTINATION QUEUE	MOVEMENT DESTINATION QUEUE	CRC1	CRC2	DISCARD FLAG
(ATMVC)	ATM		(OUTPUT QUEUE) (NUMBER)				
(IPVC)	IP		(PACKET QUEUE) (NUMBER)	(OUTPUT QUEUE) (NUMBER)			

FIG. 18

OUTPUT QUEUE NUMBER	QUALITY PARAMETER	TRANSMISSION VPI / VCI	OUTPUT DESTINATION INFORMATION

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FIG. 19

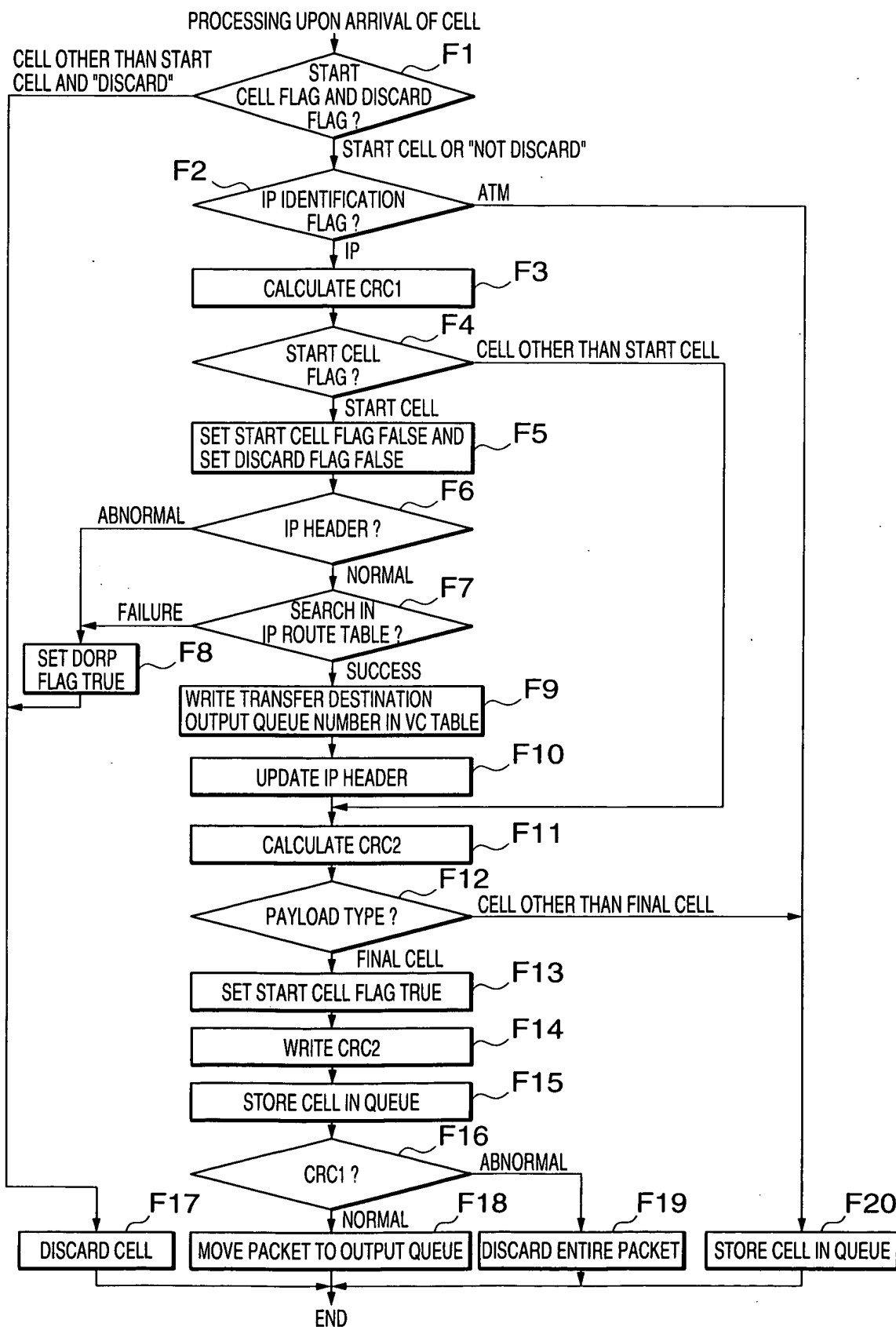


FIG. 20

PROCESSING UPON ARRIVAL OF CELL

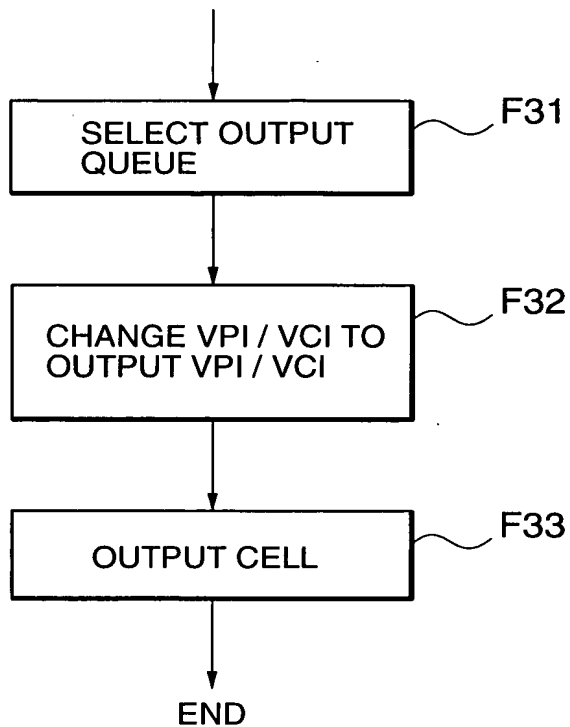
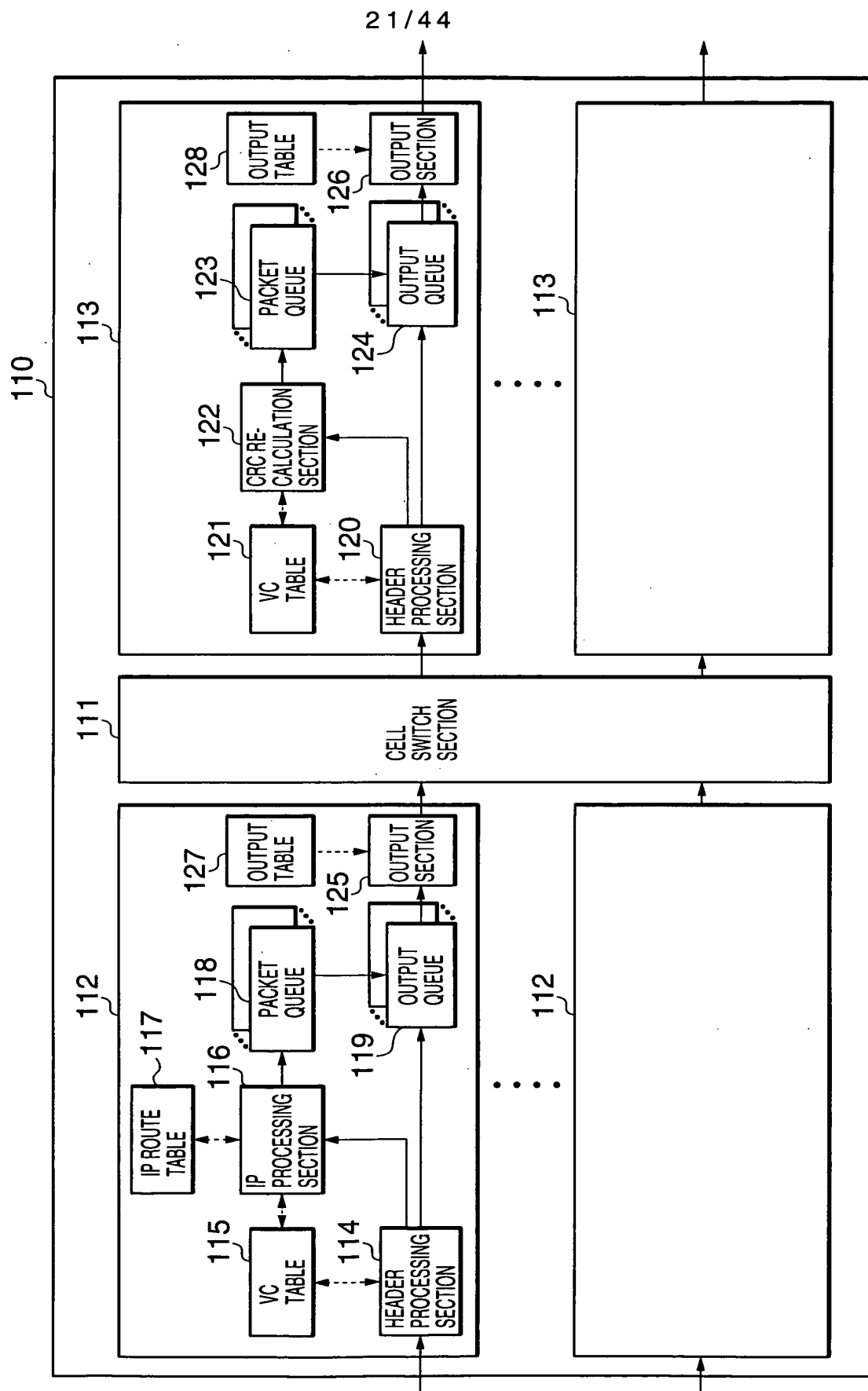


FIG. 21



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FIG. 22

SOURCE VPI / VCI	IP IDENTIFICATION FLAG	START CELL IDENTIFICATION FLAG	OUTPUT DESTINATION QUEUE	MOVEMENT DESTINATION QUEUE	CRC1	DISCARD FLAG
(ATMVC)	ATM		(OUTPUT QUEUE NUMBER)			
(IPVC)	IP		(PACKET QUEUE NUMBER)	(OUTPUT QUEUE NUMBER)		

[illegible]

OUTPUT QUEUE NUMBER	QUALITY PARAMETER	INTERNAL IDENTIFIER	OUTPUT DESTINATION INFORMATION

FIG. 24

INTERNAL IDENTIFIER	IP IDENTIFICATION FLAG	OUTPUT DESTINATION QUEUE	MOVEMENT DESTINATION QUEUE	CRC2
(ATM INTERNAL IDENTIFIER)	ATM	(OUTPUT QUEUE NUMBER)		
(IP INTERNAL IDENTIFIER)	IP	(PACKET QUEUE NUMBER)	(OUTPUT QUEUE NUMBER)	

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FIG. 25

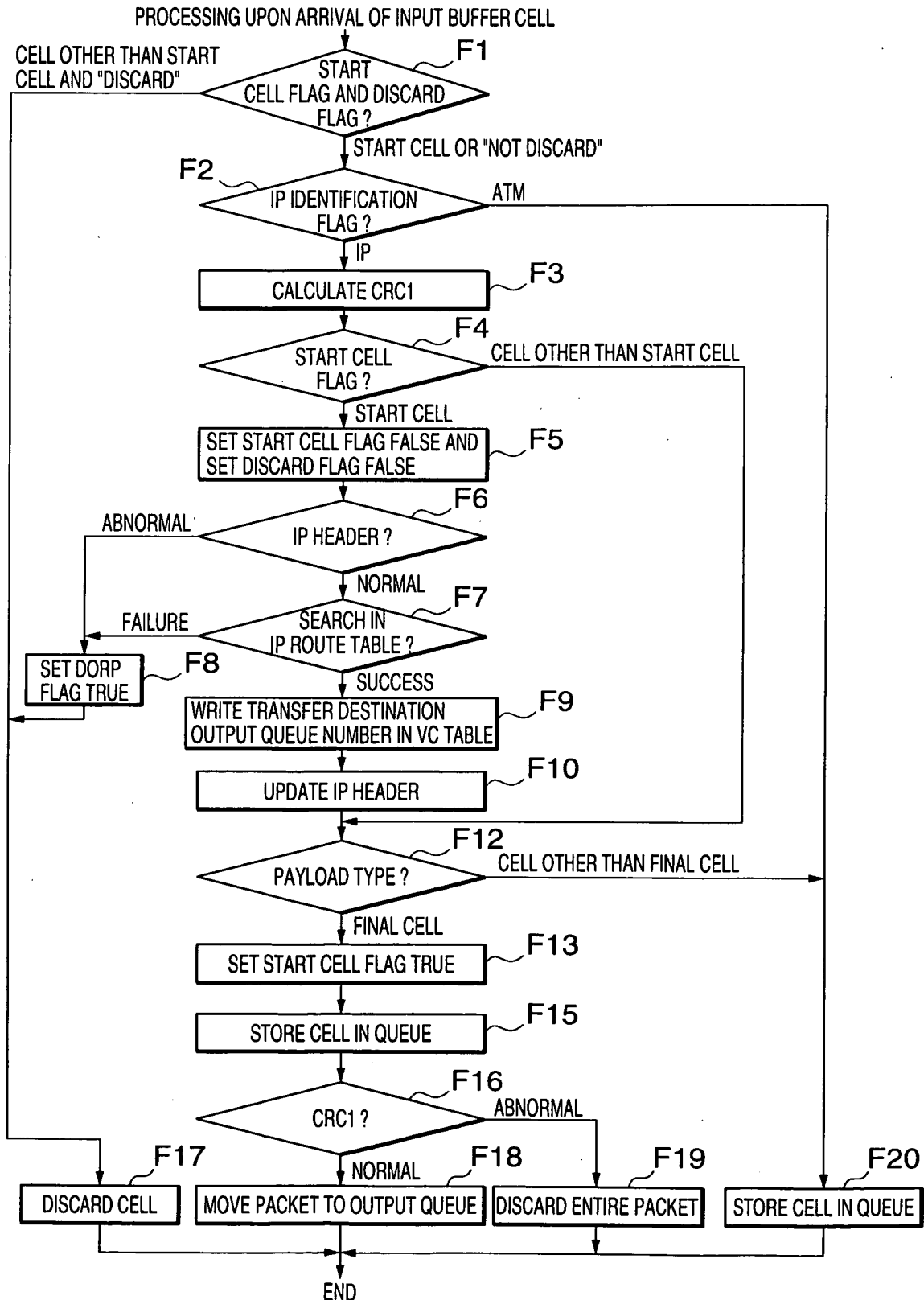


FIG. 26

PROCESSING UPON ARRIVAL OF CELL

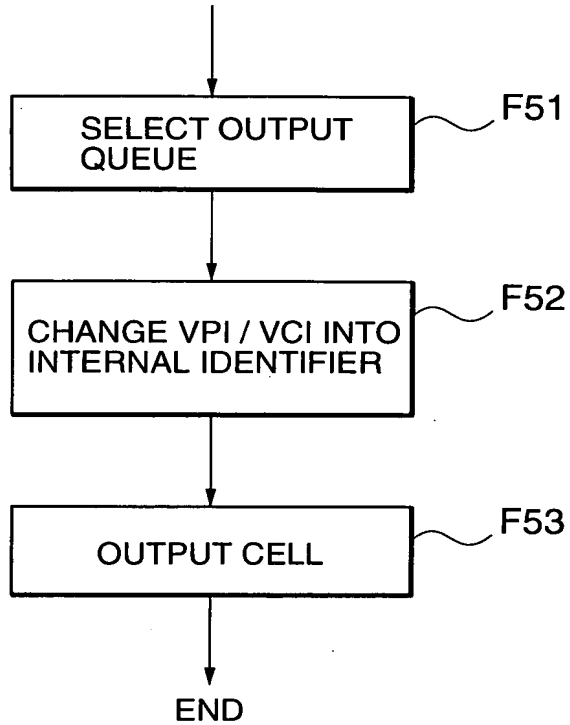


FIG. 27

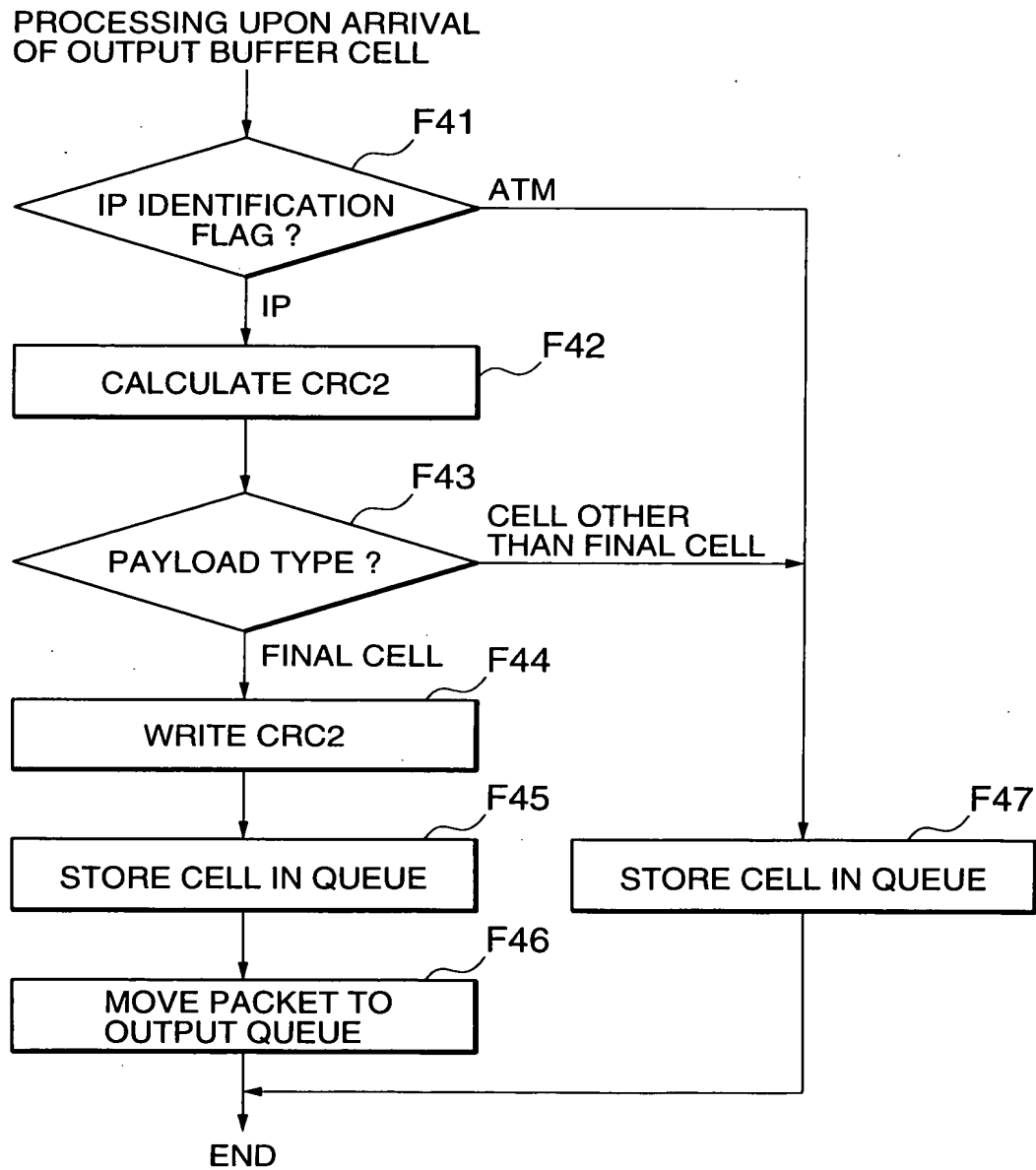


FIG. 28

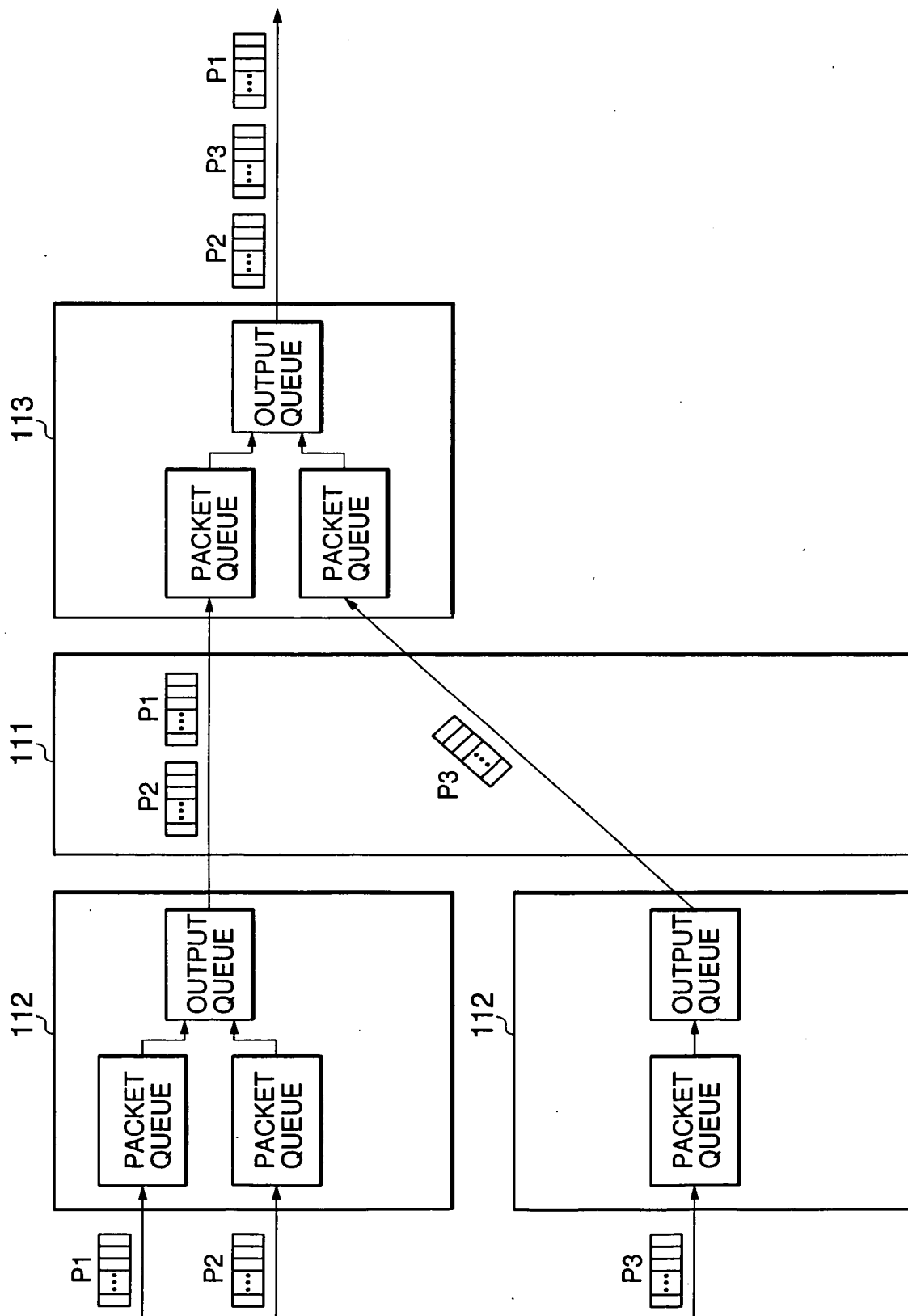


FIG. 29

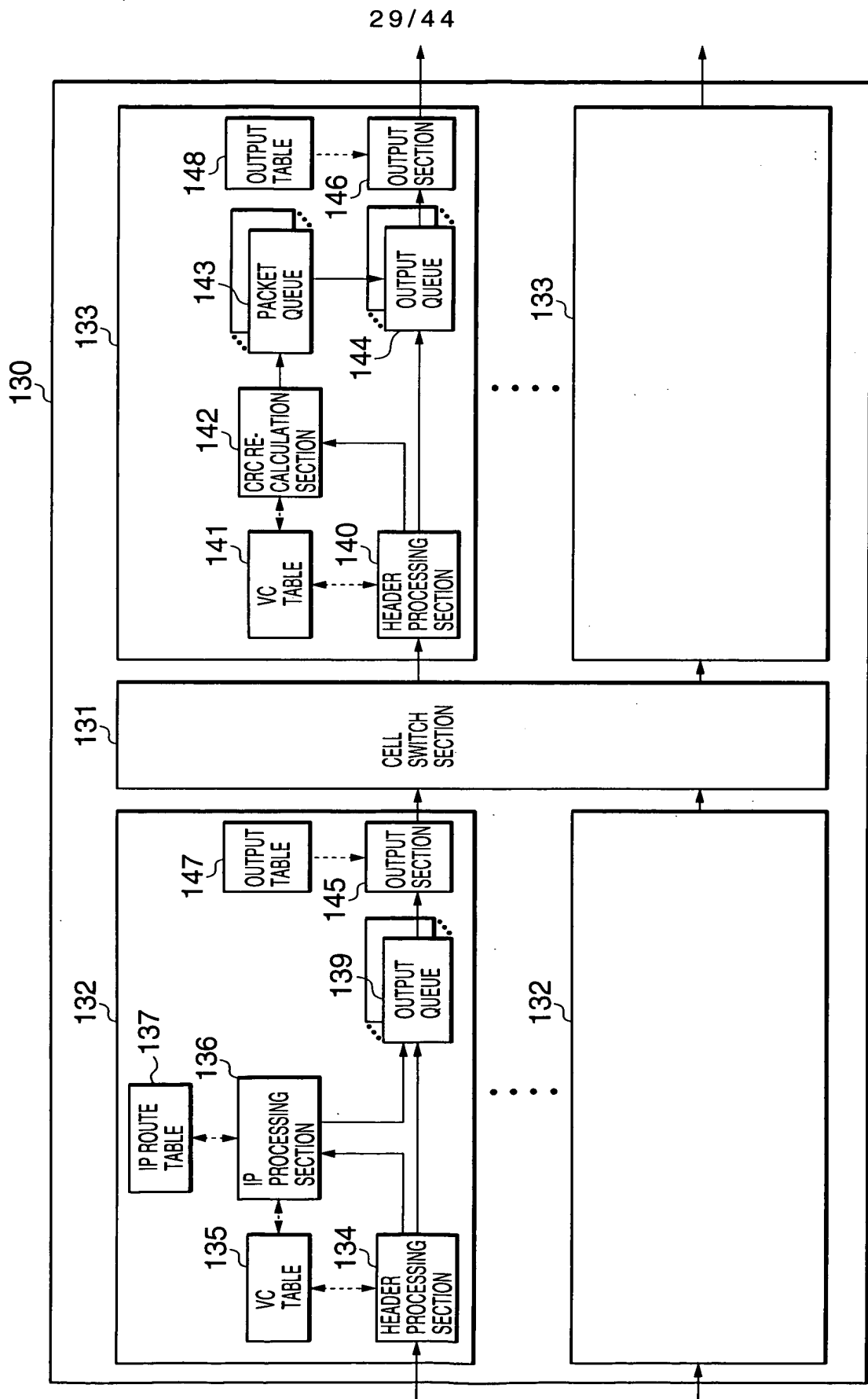


FIG. 30

SOURCE VPI / VCI	IP IDENTIFICATION FLAG	START CELL IDENTIFICATION FLAG	OUTPUT DESTINATION QUEUE	CRC1	DISCARD FLAG
(ATMVC)	ATM		(OUTPUT QUEUE) (NUMBER)		
(IPVC)	IP		(OUTPUT QUEUE) (NUMBER)		

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FIG. 31

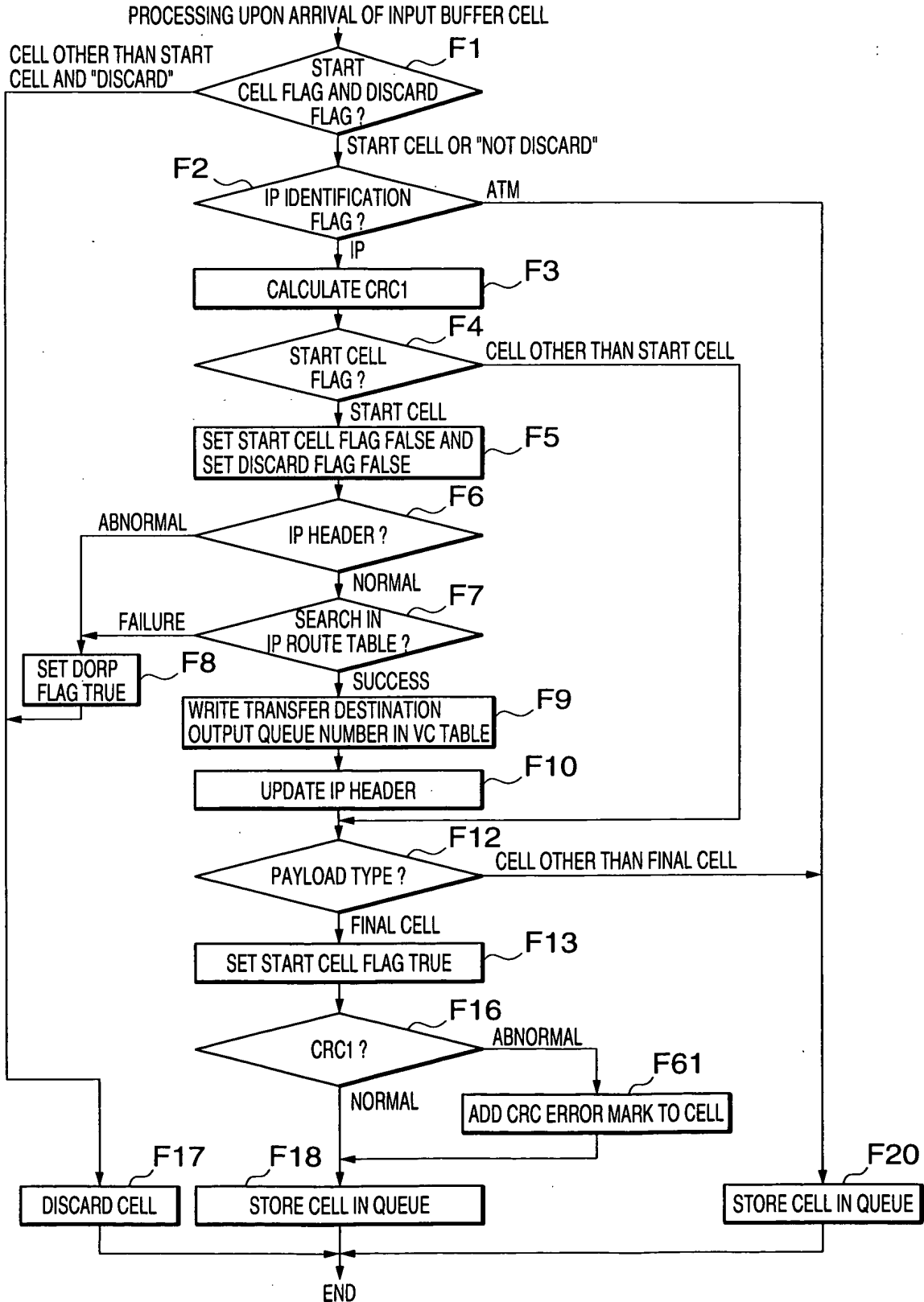


FIG. 32

PROCESSING UPON ARRIVAL
OF OUTPUT BUFFER CELL

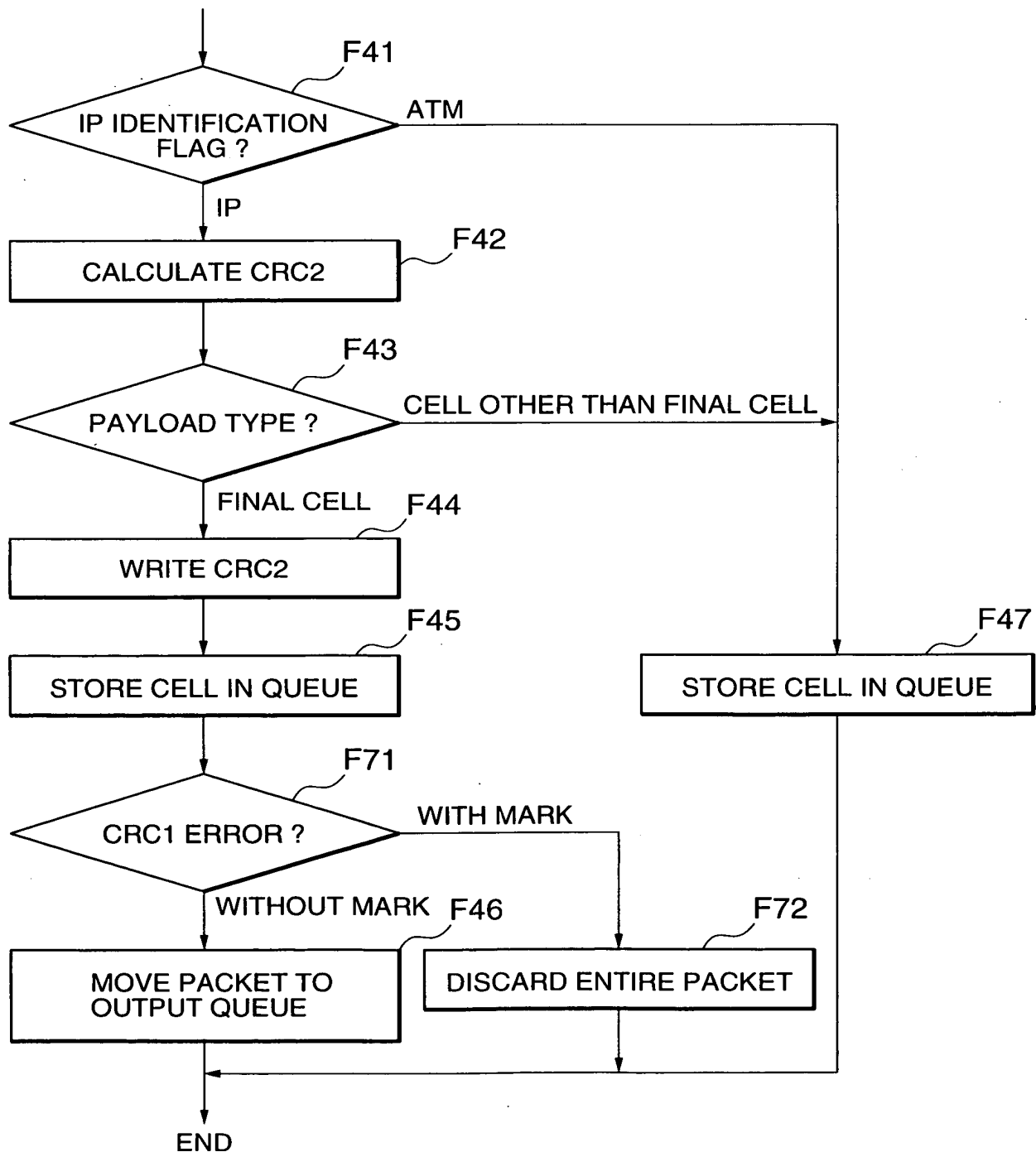


FIG. 33

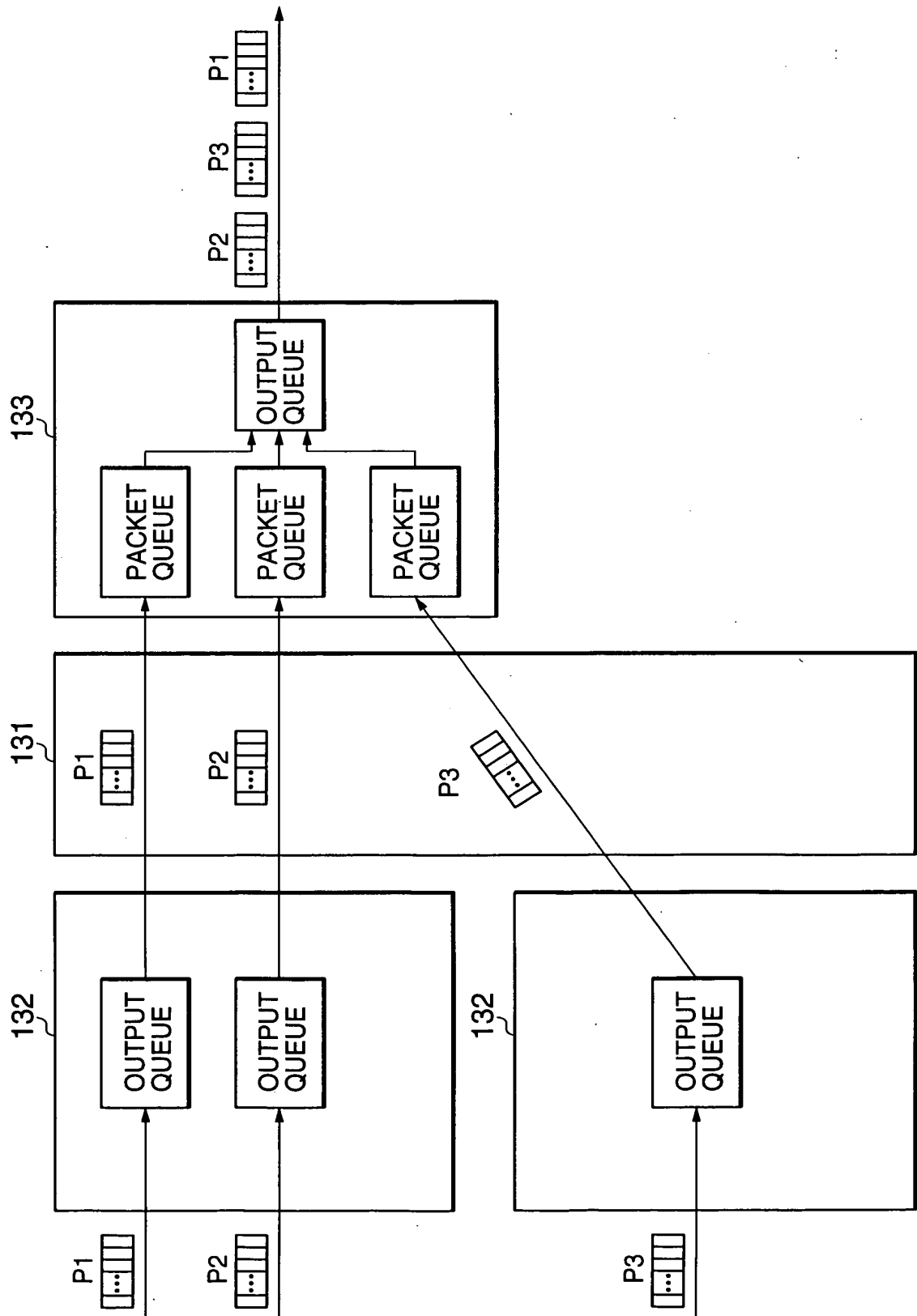


FIG. 34

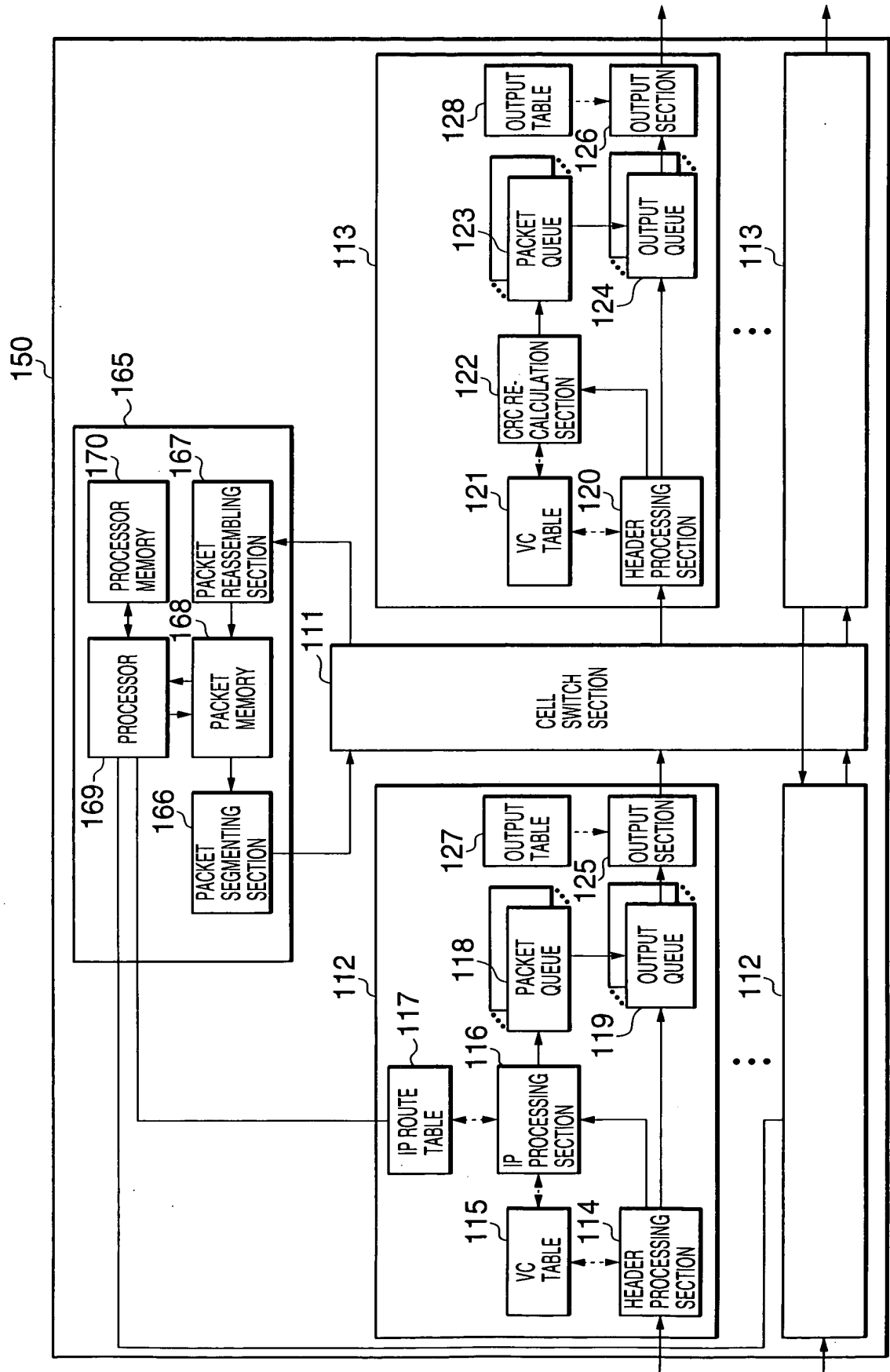


FIG. 35

DESTINATION IP ADDRESS	MASK LENGTH	OUTPUT BUFFER SECTION NUMBER	INTERNAL IDENTIFIER

FIG. 36

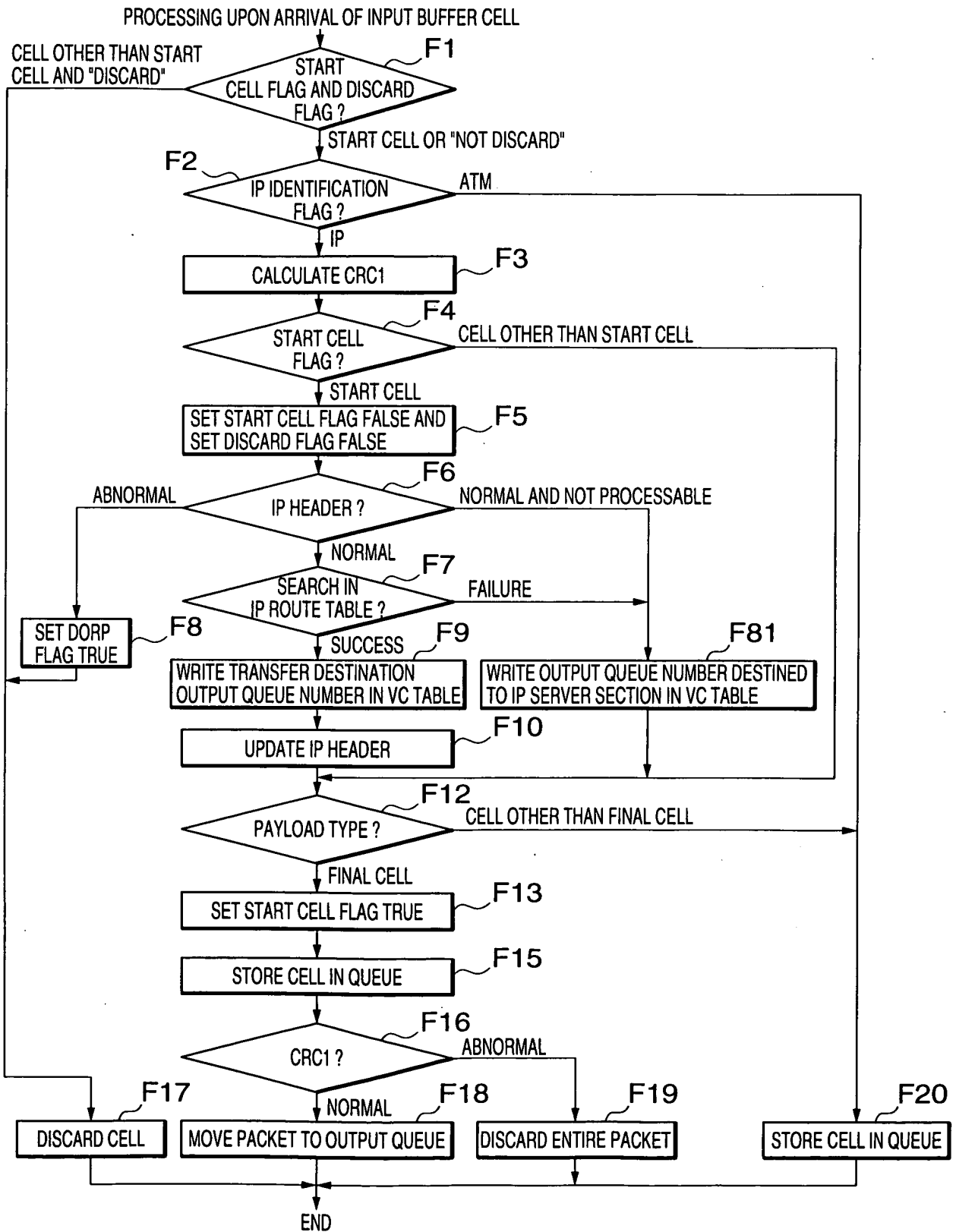


FIG. 37

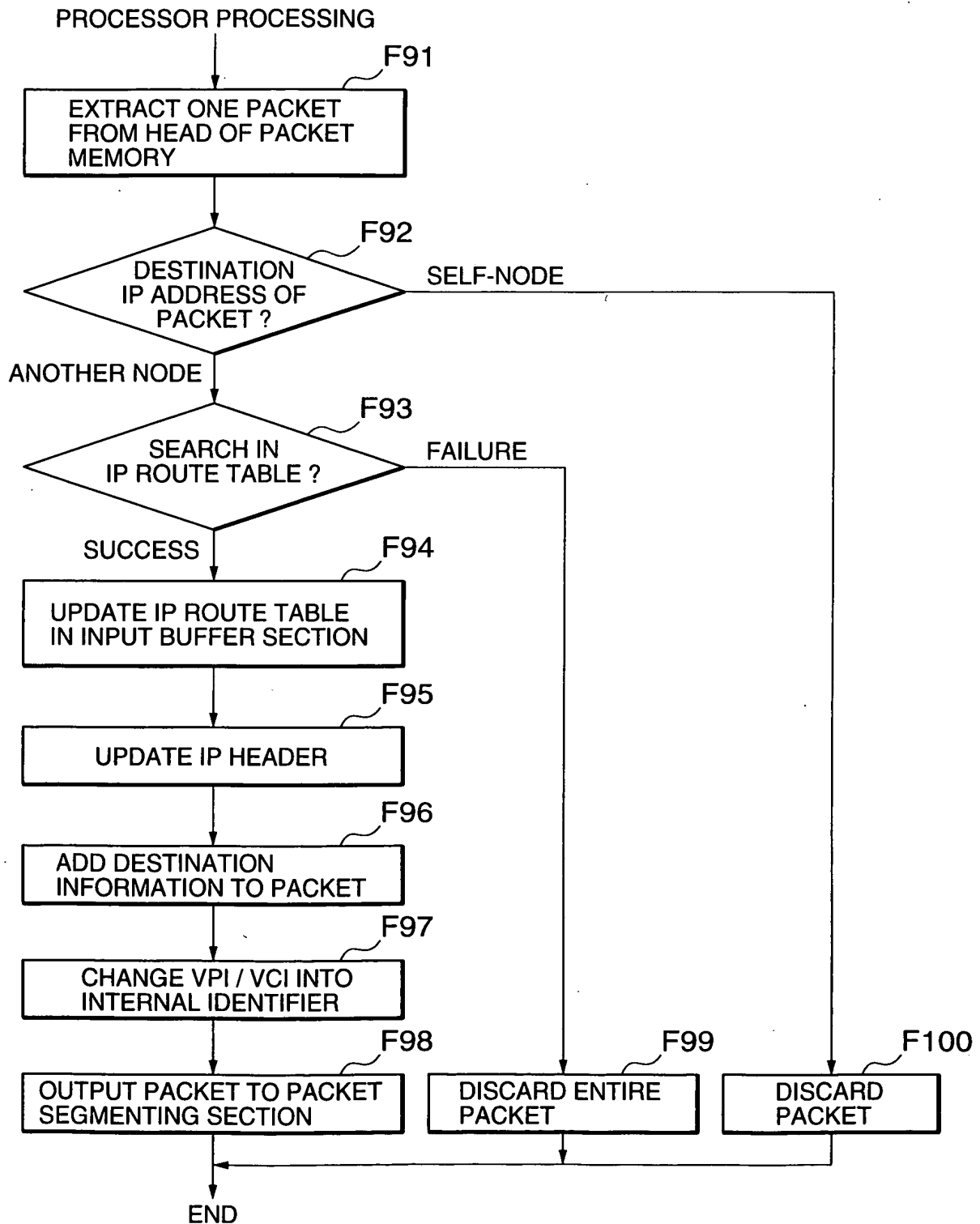
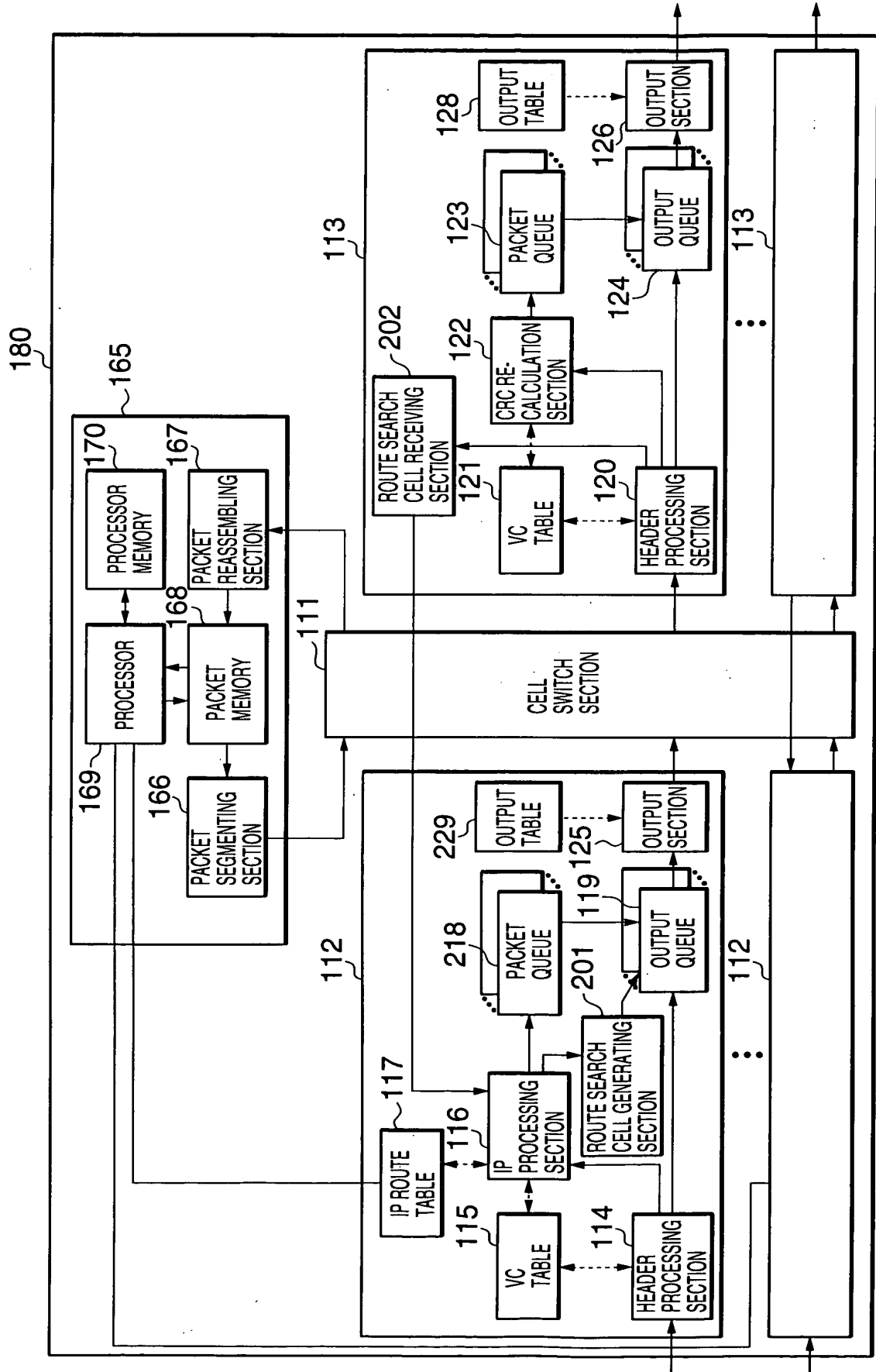


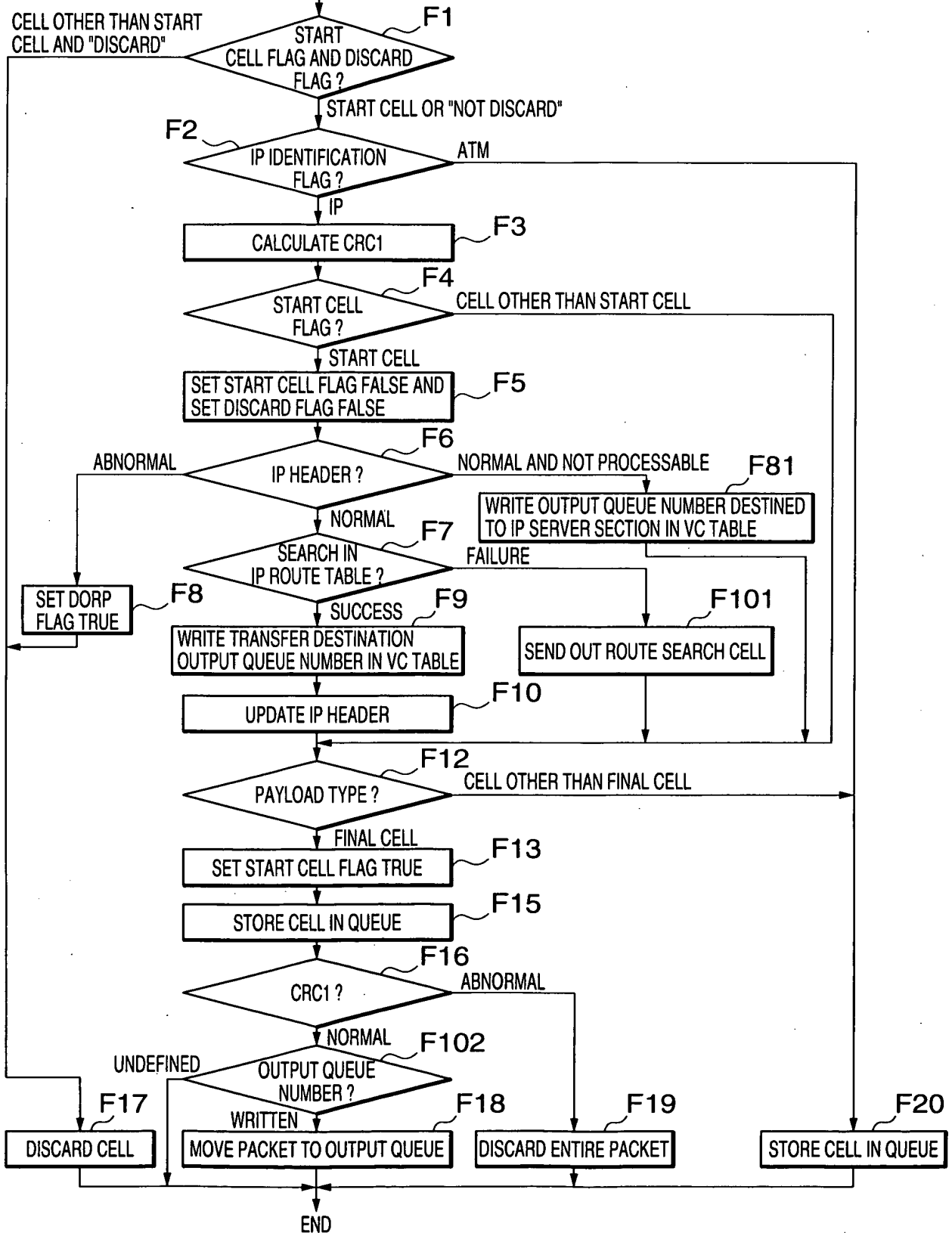
FIG. 38



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FIG. 39

PROCESSING UPON ARRIVAL OF INPUT BUFFER CELL



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FIG. 40

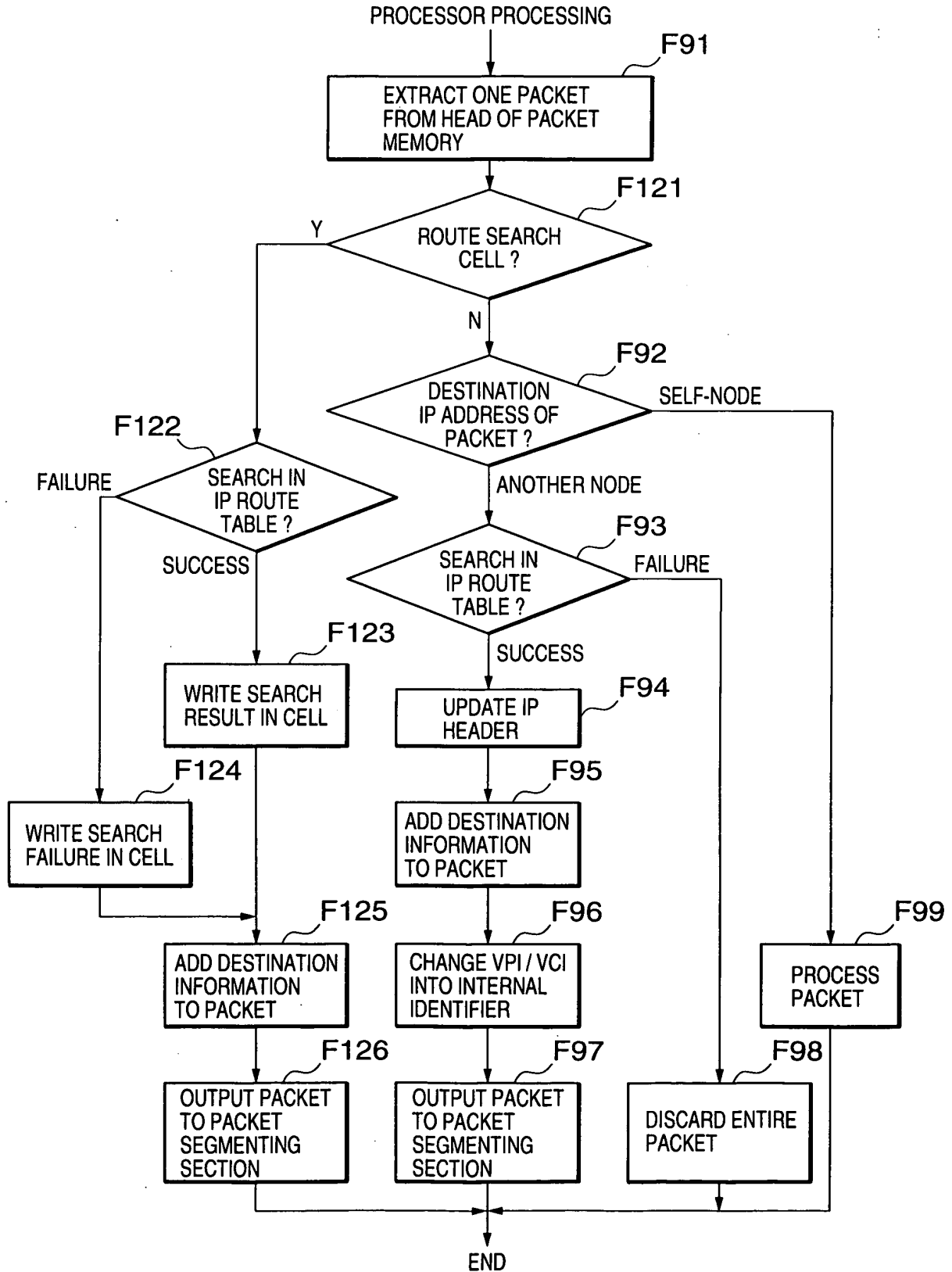


FIG. 41

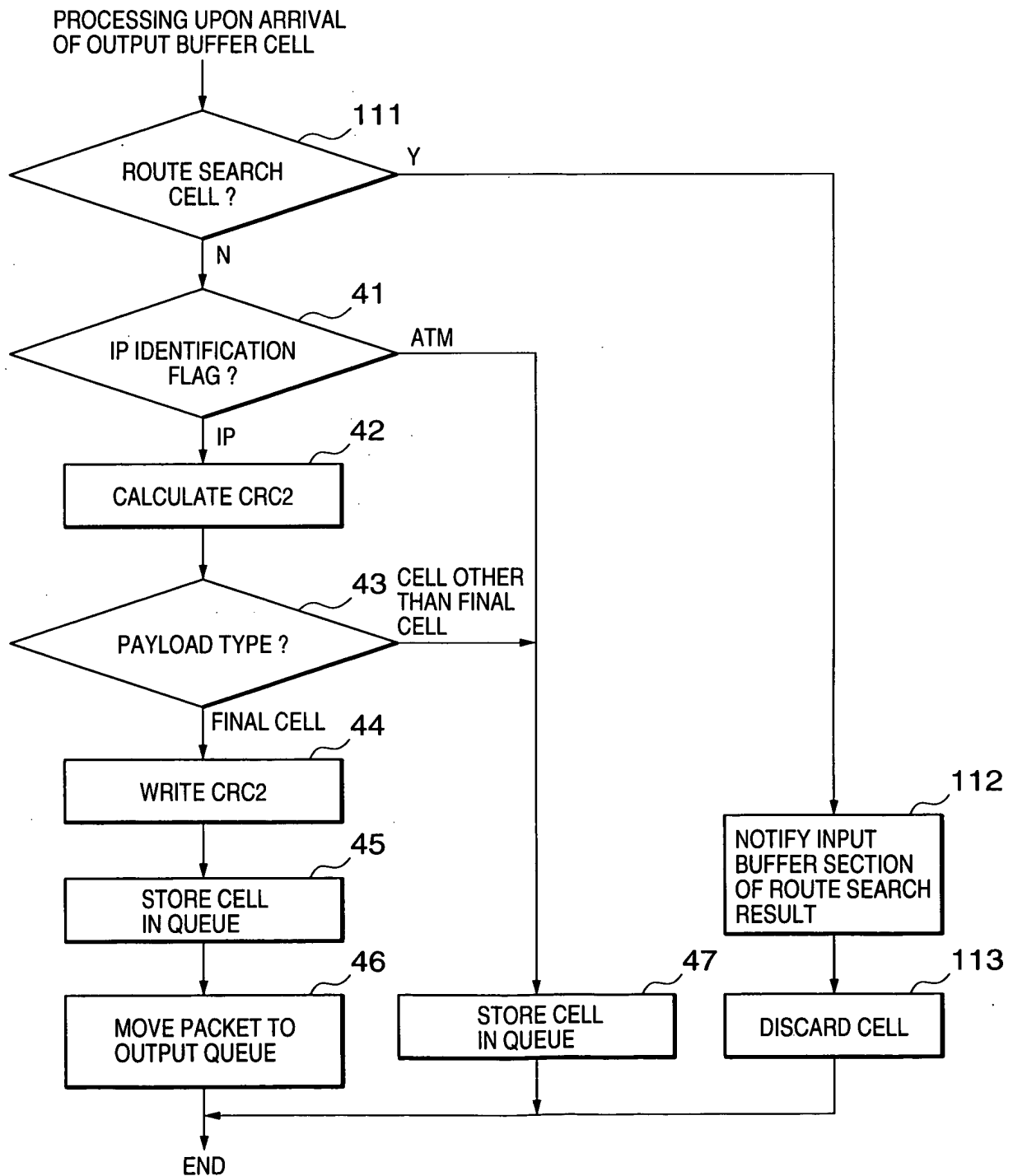


FIG. 42

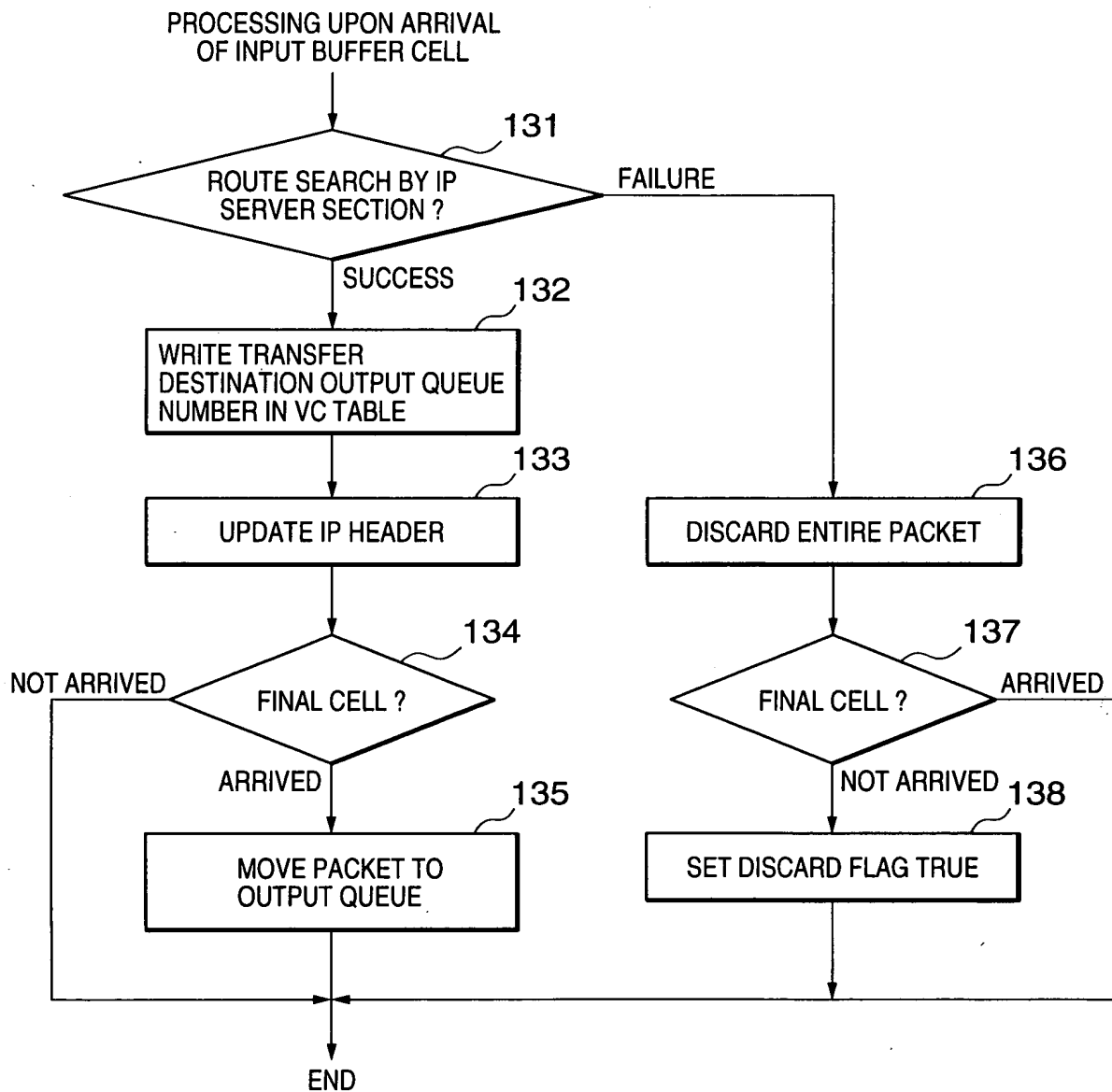


FIG. 43

SOURCE VPI / VCI	IP IDENTIFICATION FLAG	VIRTUAL DEDICATED NETWORK NUMBER	START CELL IDENTIFICATION FLAG	OUTPUT DESTINATION QUEUE	MOVEMENT DESTINATION QUEUE	CRC1	DISCARD FLAG
(ATMVC)	ATM			(OUTPUT QUEUE) (NUMBER)			
(IPVC)	IP			(PACKET QUEUE) (NUMBER)	(OUTPUT QUEUE) (NUMBER)		

FIG. 44

VIRTUAL DEDICATED NETWORK NUMBER	DESTINATION IP ADDRESS	MASK LENGTH	OUTPUT QUEUE NUMBER